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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ko Miyazaki et al
Serial No.: 09/964,490
Filed: September 28, 2001
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Art Unit: 2125
Examiner: Shechtman, S.

RESPONSE TO NON-COMPLIANT AMENDMENT

Mail Stop: Amendment
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 18, 2005

Sir:

This is in response to the Notice of Non-Compliant Amendment mailed April 18, 2005, in connection with the above-identified application, applicants are submitting herewith the following corrected section of the non-compliant amendment:

Amendments to the Claims; and

Remarks are included following the amendments.